

A "capacitor" is two electrical conductors separated by an insulator. The conductors are called "plates" because they are sometimes flat. If electrons are removed somehow from one of the plates and deposited on the other one then the first plate will have a \_\_\_\_tive charge and the second will have a \_\_\_\_tive charge, because electrons are \_\_\_\_tively charged. There is no way for the electrons to return without leaving the capacitor, travelling through an external circuit, and then re-entering.

- Imagine an uncharged capacitor with plates labelled "A" and "B":  
What happens if we remove some electrons from A and deposit them on B?
  - Electron pressure in A will \_\_crease.
  - Electron pressure in B will \_\_crease.
  - The pressure difference between A and B will \_\_crease.
  - The potential difference between A and B will \_\_crease.
- If the plates are initially neutral (as in #1) the process of removing electrons from one plate and depositing electrons on the other plate is called "charging" the capacitor.
  - In 1c you figured out that charging a capacitor causes its voltage to \_\_crease.
  - When we *discharge* a capacitor its voltage must \_\_crease. (Recorded in # \_\_ on RS \_\_.)
- Capacitors can be constructed by placing a thin layer of insulating material such as plastic or aluminum sulfide between two sheets of aluminum foil.
  - In circuit diagrams a capacitor's plates are represented by two straight, parallel lines with equal lengths, drawn close together. *Draw such a pair in the space at the right.*
  - The wires connecting those two plates to the rest of the circuit are called "leads" and are represented by straight lines outward from the centers of those two plates. *Label the plates and leads clearly in your diagram at the right.*
- In the space at the right, draw a resistor and a capacitor connected in series to a battery.
  - Draw arrows *on the capacitor leads* in your diagram to indicate the expected direction of electron flow in them if the capacitor is initially *uncharged*.
  - We expect this capacitor voltage to \_\_crease gradually, as in part \_\_ of #2.
  - How must the three voltages in this circuit be related? \_\_\_\_ = \_\_\_\_ + \_\_\_\_  
(Use page 83 or 83R or Review Sheet VIII.)
  - Define the symbols in equation 4c.
  - Do you expect the battery's emf to change during the charging process? \_\_\_\_
  - Do you expect the resistor voltage to change? \_\_\_\_ -If so, which way? \_\_\_\_ (Use 4b, 4c & 4e.)
  - If you answered 4e or 4f with a "yes", please explain why, starting with 4b & 4c.
- Suppose we want the charging process in #4 to be very slow:
  - We must then choose a resistor with very \_\_\_\_\_ resistance. (small, great) (See #2 on RS IX.)
  - Which device below could be used as a resistor to satisfy that requirement best?
 

ammeter	voltmeter	wire	spring scale
light bulb	capacitor	branch point	battery
- Use #4 and #5 to make a prediction about the *slope* of the *capacitor voltage vs time* graph for the circuit in #5. Use the special "rate" symbol on RS V, as originally explained on page 54. Also remember to put a subscript into your statement so that you will know which voltage it describes.  

" \_\_\_\_ is \_\_\_\_tive when the capacitor is \_\_\_\_\_ing"
- In one kind of capacitor the insulator is a very thin film of an aluminum compound deposited electrolytically onto one of the foil sheets. If such an "electrolytic" capacitor is charged with the wrong polarity, that chemical process is reversed, and the capacitor is damaged. *WHEN USING AN ELECTROLYTIC CAPACITOR, AVOID GIVING THE "PLUS" PLATE A NEGATIVE CHARGE.* (That's the "**sign rule**" for electrolytic capacitors".)
- Copies of #2, 4, 5, 6, and 7 have been saved on RS \_\_\_\_ . *You'll need them on pages 97 and 98.*

- \* 1. Draw a voltmeter and a capacitor connected in series to a power supply, as in #4 & 5 on p. 96. Put signs on the capacitor and voltmeter terminals. After your diagram has been approved, find out if your predictions about increasing and decreasing voltages were correct. *Watch the voltmeter after you complete the circuit.* Write three sentences beside your diagram describing what happens to each of the three voltages in this circuit. (Use #5 on RS IX, as we did in #4 on p. 96.) Then do #2.
2. Replace the voltmeter in your circuit with a flashlight bulb, diagram the new circuit and then do #3.
- \* 3. Unplug circuit #2 from the power supply. Connect the two plugs together so that electrons can flow through the light bulb from one capacitor plate to the other. Then plug it in again, as in #2.
- Repeat both steps several times and describe what you see. Also try it with a voltmeter, as in #1.
  - Illustrate both of your descriptions with circuit diagrams. Make the capacitor leads *more than one centimeter long* in these diagrams and *label* them as you did in 3b on p. 96. *Use the sign rule.*
  - Electrons always flow from regions with \_\_\_\_\_ pressure into regions with \_\_\_\_\_ pressure, if they can.
  - Put *signs* on the capacitor plates and put *arrows* on the capacitor leads in the diagrams in 3b & 4, showing the directions of electron flow in both circuits. Don't contradict 3c.
- \* 4. Make circuit 1 again. Without unplugging anything, connect a bulb in parallel with the voltmeter. (Hold its leads in your hands.) Then connect the bulb across the capacitor. Repeat both steps several times, observing what happens. Draw *two* circuit diagrams showing what you did. *Make both capacitor leads and both voltmeter leads long and straight.* Describe what happens to *each* voltage in *each* case. Use "increases", "decreases", or "remains unchanged" as in #1.
5. A "significant" current is enough to light a flashlight bulb. An "insignificant" current can make the voltmeter needle move, but is *not* enough to light the bulb. The current in the leads of the \_\_\_\_\_ was *insignificant* at all times in #4, because that device has a very \_\_\_\_\_ resistance. (See #2 on RS IX.) Using what you said about input and output currents in #3 on RS IX, make *dots* on *EACH* wire which carries insignificant current in the two diagrams for #4. *A branch point is not a wire.*
6. In 3 & 4 we discovered that the capacitor *charges* rapidly when the light bulb is connected across the \_\_\_\_\_, and it *discharges* rapidly when the bulb is connected across the \_\_\_\_\_.
- When you stop charging the capacitor and begin discharging it, the *direction* of electron flow in the capacitor leads is \_\_\_\_\_ed. -Does this contradict # 3 or 4 above? \_\_\_\_
  - Using those clues, indicate the direction of electron flow in the capacitor leads *AND* in the light bulb in *BOTH* diagrams in #4. *DO NOT put arrows on the dotted wires. Use diodes to check.*
  - Whenever the bulb is lit, the bulb current must be almost equal to the \_\_\_\_\_ current. (Use 6b.)
  - Does 6c contradict 6b or the branch point law? \_\_\_\_ Does 6b agree with #5? \_\_\_\_
  - Whenever the capacitor is *charging*, its voltage is \_\_\_\_\_creasing. Do #1 & 4 agree with 6e? \_\_\_\_
  - During the *charging* process the resistor voltage \_\_\_\_\_creases, as in #4 on p. 96. Does #1 agree? \_\_\_\_
7. In #1 the capacitor charged very \_\_\_\_\_ly (quickly, slowly) because the voltmeter in the circuit had very great \_\_\_\_\_. *Does #5 agree?* \_\_\_\_ The capacitor charges and discharges more \_\_\_\_\_ly in #4 because the \_\_\_\_\_ has much less resistance than the \_\_\_\_\_, as we saw on pages 84 and 90. Do these answers contradict 6c? \_\_\_\_ Do the dots in #5 agree with #7? \_\_\_\_
8. **Sign convention for currents:** The current in the capacitor leads is in the "*positive*" direction when the capacitor is *charging*. The capacitor current is in the \_\_\_\_\_tive direction when the capacitor is *discharging*. That rule is in #\_\_ on RS \_\_. Do #3 and 6a agree with it? \_\_\_\_ -Does 6b? \_\_\_\_
9. In #4, the capacitor and light bulb currents changed at the moment when the light bulb was connected. Did they change suddenly? \_\_\_\_ -Did they change gradually? \_\_\_\_ *Explain on the back.*
10. Unplugging a light bulb makes it stop glowing *suddenly*. In #2, 3 and 4 the bulb dimmed out \_\_\_\_\_ly. (suddenly, gradually) This shows that the currents \_\_\_\_\_ly approached \_\_\_\_\_ as the light dimmed, so all of the graphs must approach constant "asymptotic" values as the light dims.
- If the current-time graph approaches that value from below, it must be "concave \_\_\_\_\_ward".
  - If it approaches that same value from above, it must be "concave \_\_\_\_\_ward".
  - Sketch graphs with *reasonable* initial values to illustrate answers 10a and 10b. Label them clearly.
11. After the light went out in #4 it was disconnected in preparation for the next experiment. Did that action cause any sudden changes in voltage or current? \_\_\_\_ *If so, please explain and illustrate.*

1. In #3 on page 97 you saw how a capacitor can be used instead of a battery to supply energy to a light bulb. Sketch a diagram of that circuit in the space at the right. Put signs on the capacitor plates.
  - a. Copy the beginner's definition of "voltage" from RS IX: \_\_\_\_\_
  - b. Will there be any voltage between the bulb terminals if the capacitor is charged? \_\_\_\_\_
  - c. Will that voltage cause a current through the light bulb? \_\_\_\_\_ (Use # 14 on RS IX.)
  - d. Electrons are attracted by the \_\_\_\_\_ tive plate and repelled by the \_\_\_\_\_ tive plate. (#9 on RS IX)
  - e. Draw arrows in your diagram showing which way electrons must flow through the light bulb.
  - f. The capacitor voltage \_\_\_\_\_ creases during this process.
  - g. The bulb voltage must \_\_\_\_\_ crease at the same time, so the bulb current \_\_\_\_\_ creases. (See #1 on RS X)
  - h. Some light came out of the bulb. Where was that energy stored before the circuit was completed?
  
2. Draw a resistor and capacitor connected in series to a battery. Let " $V_r$ " represent the resistor voltage, let " $V_c$ " represent the capacitor voltage, and let " $V_b$ " represent the battery voltage.
  - a. How must those voltages be related in *this* circuit? (Use #5 on RS IX.) \_\_\_\_\_ + \_\_\_\_\_ = \_\_\_\_\_
  - b. According to the charge conservation law, (#3 on RS IX) how must the input and output currents be related for *any* electrical component? \_\_\_\_\_ *That's a clue for #2c!*
  - c. What can we conclude about the number of electrons in this capacitor? \_\_\_\_\_ (Increasing? -Decreasing? -Remaining constant? -Converted into joules?)
  - d. The \_\_\_\_\_ tive terminal of the battery pulls electrons in. -Which one *emits* electrons? \_\_\_\_\_
  - e. We cannot alter the battery. But we *can* increase the current in this circuit by reducing the \_\_\_\_\_ of the \_\_\_\_\_ or by increasing the \_\_\_\_\_ across the \_\_\_\_\_. (See #2 & 5 on RS X.)
  - f. Connecting a light bulb in parallel with the resistor is equivalent to \_\_\_\_\_ creasing the resistance of the resistor, as in 2e. -Does this agree with #4 on page 90? \_\_\_\_\_
  - g. When we made that change (in #5 on page 97) the capacitor's charging rate must have \_\_\_\_\_ creased. Does this contradict 2e? \_\_\_\_\_ *If so, explain why you have not corrected that blunder.*
  - h. A capacitor's "charging rate" is the slope of its \_\_\_\_\_ vs. \_\_\_\_\_ graph. (Saved in #\_\_ on RS XI.)
  - i. *By definition* that rate is equivalent to the \_\_\_\_\_. (Check units and see 1b on page 88.)
  - \*j. Why do the resistor voltage and current *always* decrease during the charging process, *regardless* of whether the bulb is present or absent? *Answer with a statement that is always true.* (Use 2a.)
  
3. Let's predict the shapes of four graphs for the "charging circuit" described in #2: "Charge vs. Time", "Capacitor Voltage vs. Time", "Resistor Voltage vs. Time", and "Current vs. Time". The capacitor's "charge" means the charge *on one plate*. (The other plate has an equal but opposite amount, as you showed in #2c above.) Draw and label axes for the four graphs on the back of this paper.
  - a. If the capacitor is initially uncharged then the initial value of the capacitor voltage must be \_\_\_\_\_.
  - b. Using 2a, we see that the resistor voltage graph must begin \_\_\_\_\_ the origin. (at, above, below)
  - c. Ohm's equation then tells us that the current graph must begin \_\_\_\_\_ the origin.
  - d. Using 3a-c, make a dot on each graph's vertical axis to indicate its initial value.
  - e. According to 2h, the initial slope of the first graph must be \_\_\_\_\_ tive.
  - f. In #1 on RS XI (#2 on page 96) you said the capacitor's voltage depends on its \_\_\_\_\_. The initial slope of the capacitor voltage graph must be \_\_\_\_\_ tive. *Illustrate 3e & 3f with short line segments.*
  - g. According to 2j and 3f, the initial slope of the third graph must be \_\_\_\_\_ tive.
  - h. The familiar equation mentioned in 3c relates the third and fourth graphs: ( \_\_\_\_\_ = \_\_\_\_\_ )
  - i. Eq. 3h tells us that the initial slope of the fourth graph must be \_\_\_\_\_ tive. Make short line segments on the 3rd & 4th graphs to illustrate 3g and 3i. -Do they contradict 3d or 3e? \_\_\_\_\_
  - j. What definition mentioned in 2h & 2i relates the fourth graph to the *first* graph? \_\_\_\_\_ = \_\_\_\_\_
  - k. 3i and 3j tell us that the first graph must be *concave* \_\_\_\_\_ ward. With 3f we see that the second graph must be *concave* \_\_\_\_\_ ward. *Illustrate by extending the first two graphs.* (See #10 on p. 98.)
  - l. The third and fourth graphs must be *concave* \_\_\_\_\_ ward to satisfy 2a. After a long time has passed, each graph must resemble a \_\_\_\_\_ al line. *Use those facts to finish sketching the four graphs.*
  
4. Copy the two circuit diagrams that you drew for #4 on page 97. Label one as the "charging" circuit and the other as "discharging". For each one sketch graphs of *Capacitor Voltage vs Time*, *Light Bulb Current vs Time*, and *Capacitor Current vs Time*. Label all *ALL SIX* graphs clearly, using these clues:
  - a. **Clue 1:** Each graph must have *THREE SECTIONS*, showing conditions *before, during, and after* the flash. *Make vertical dotted lines to indicate the boundaries between those sections.*
  - b. **Clue 2:** Descriptions of those graphs were written on pages 96 and 97. **Clue 3:** In #2 on RS XI there is a sign convention for capacitor currents. **Clue 4:** Don't contradict 1e, 1f, 1g, or 3 on this page.